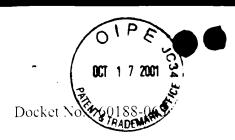
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PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of

Bunji MIZUNO, et al.

Serial No.: 09/840,306

Filed: April 24, 2001

Group Art Unit: 1763

Examiner:

METHOD OF MANUFACTURING SEMICONDUCTOR DEVICE

REQUEST FOR CORRECTED FILING RECEIPT

Commissioner for Patents Washington, DC 20231

Sir:

For:

Attached is a copy of the Filing Receipt received from the U.S. Patent and Trademark Office in the above-referenced application. It is noted that the number of total is incorrect on the filing receipt. Please correct the number of total claims to reflect: 38. It is requested that a corrected filing receipt be issued.

Respectfully submitted,

MCDERMOTT, WILL & EMERY

Michael £ Fogarty Registration No. 36,139

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Date: October 17, 2001 Facsimile: (202) 756-8087



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NO STAIM ATTY DOCKET NO DRAWINGS GRP ART UNIT FIL FEE RECD APPLICATION NUMBER FILING DATE 10 1368 60188-063 04/24/2001 1763 09/840,306

CONFIRMATION NO. 4390

UPDATED FILING RECEIPT

*OC000000006852528

Date Mailed 10/05/2001

Receipt is acknowledged of this nonprovisional Patent Application. It will be considered in its order and you will be notified as to the results of the examination. Be sure to provide the U.S. APPLICATION NUMBER. FILING DATE NAME OF APPLICANT, and TITLE OF INVENTION when inquiring about this application. Fees transmitted by check or draft are subject to collection. Please verify the accuracy of the data presented on this receipt. If an error is noted on this Filing Receipt, please write to the Office of Initial Patent Examination's Customer Service Center. Please provide a copy of this Filing Receipt with the changes noted thereon. If you received a "Notice to File Missing Parts" for this application, please submit any corrections to this Filing Receipt with your reply to the Notice. When the USPTO processes the reply to the Notice, the USPTO will generate another Filing Receipt incorporating the requested corrections (if appropriate).

Applicant(s)

Bunji Mizuno, Nara, JAPAN; Hiroaki Nakaoka, Kyoto, JAPAN; Michihiko Takase, Nara, JAPAN; Ichiro Nakayama, Osaka, JAPAN:

Domestic Priority data as claimed by applicant

THIS APPLICATION IS A CIP OF 09/178,766 10/26/1998 ABN WHICH IS A DIV OF 08/734,218 10/21/1996 PAT 6,217,951

Foreign Applications

JAPAN 7-274234 10/23/1995

If Required, Foreign Filing License Granted 06/15/2001

Projected Publication Date: 01/10/2002

Non-Publication Request: No

Early Publication Request: No

Title

Method of manufacturing semiconductor device

Preliminary Class





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